

RapidIO® SwitchKit™ Product Brief

Features

- From two to thirty two RapidIO ports, any mix of: LP-LVDS or LP-Serial physical layer ports.
- Physical layer ports are compliant with Rev. 2.2 of the specification.
- Supports RapidIO large or small transport model.
- Multicast port group support.
- Aggregate port group support for trunking and load balancing applications.
- Separate traffic queues for each port, and priority with separate traffic queues for unicast and multicast traffic.
- Supports RapidIO Error Management Extensions
- Optional Error Detection And Correction (EDAC) on all memory structures.

Description

The switch provides wire-speed, non-blocking connectivity between RapidIO physical layer ports. The Switch Management Block handles maintenance transaction accesses to internal registers, as well as generating event notification messages.

Figure 1 illustrates the overall architecture of the switch.

Supported Standards

RapidIO 1x/4x LP-Serial Physical Layer Specification
 RapidIO Common Transport Specification
 RapidIO Error Management Extensions Specification
 RapidIO Multicast Extensions Specification

Figure 1 RapidIO Switch Block Diagram

